

I hereby declare that:

1.) I believe that I am one of the original and first inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled: "A Multiple and Parallel Access Network Tap for Gigabit Internet LANS", the specification of which was filed on Sept. 19, 2003, as the United States Application Number 10/667,107, and as amended with the papers submitted along with this declaration and petition.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, which are not amended by any amendment referred specifically above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the nation or PCT international filing date of the continuation-in-part application.

I hereby declare that all statements made therein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued therein.

Name of Additional Inventor: Eldad Matityahu Date: 5-5-2008

**Signature of Additional Inventor:**

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**City**      **State**      **Zip**      **Country**

“Timing Board”, and [18, 17] labeled “Power Supply Connections”. It is labeled “Interior top view”.

Figure 5, read left-to-right, has had [4a-4d] labeled “Monitoring Ports”, [16a-16d] labeled “Splitters”, [10b] labeled “Splitters (1 Pair)”, [10a] labeled “Splitters (1 Pair)”, [9] labeled “Power LED”, [12] labeled “Signal Regenerating Board”, [2a-2b] labeled “Network Ports”, [14] labeled “Timing Board”, and [18, 17] labeled “Power Supply Connections”. It is labeled “Interior Top View with logical cabling pathes and port offsets for clarity”.

Figure 6, read left-to-right, has had [4a-4d] labeled “Monitoring Ports”, [16a-16d] labeled “Splitters”, [10b, 10a] labeled “Paired Splitters”, [9] labeled “Power LED”, [11] labeled “Link Status LED”, [12] labeled “Board A”, [2a-2b] labeled “Network Ports”, [14] labeled “Timing Board Board B”, and [17, 18] labeled “Power Supply Connections”. It is labeled “Interior top view with logical front view”.

Figure 7, read left-to-right, has had [4a-4d] labeled “Monitoring Ports”, [16a-16d] labeled “Splitters”, [10b, 10a] labeled “Paired Splitters”, [9] labeled “Power LED”, [11] labeled “Link Status LED”, [12] labeled “Board A”, [2a-2b] labeled “Network Ports”, [14] labeled “Timing Board Board B”, and [17] labeled “Power Supply Connection”. It is labeled “Interior top view with logical front view modified for monitoring input”.

Also submitted, in anticipation of the approval of these changes as those requested by the Office, is a set of Replacement Drawings 1-7, all with the descriptive text labels included and, as directed in the Office Action, each properly labeled as a ‘Replacement Sheet’ in each top margin.

It is believed that this fully and completely complies with the changes requested by the PTO in this Office Action and thus fulfills the formal matter identified therein.

The originally-named inventor, the assignee, and the originally, and unintentionally omitted inventor, all petition that the omission be corrected and that Eldad Matityahu be added as a named inventor to this patent application and any patent that may issue, as the requisite declarations are hereby included and filed.

It is believed that this fully and completely complies with the changes requested by the PTO in this Office Action and thus fulfills the formal matter identified therein.

Respectfully Submitted:



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## Replacement Sheet—Annotated Sheets

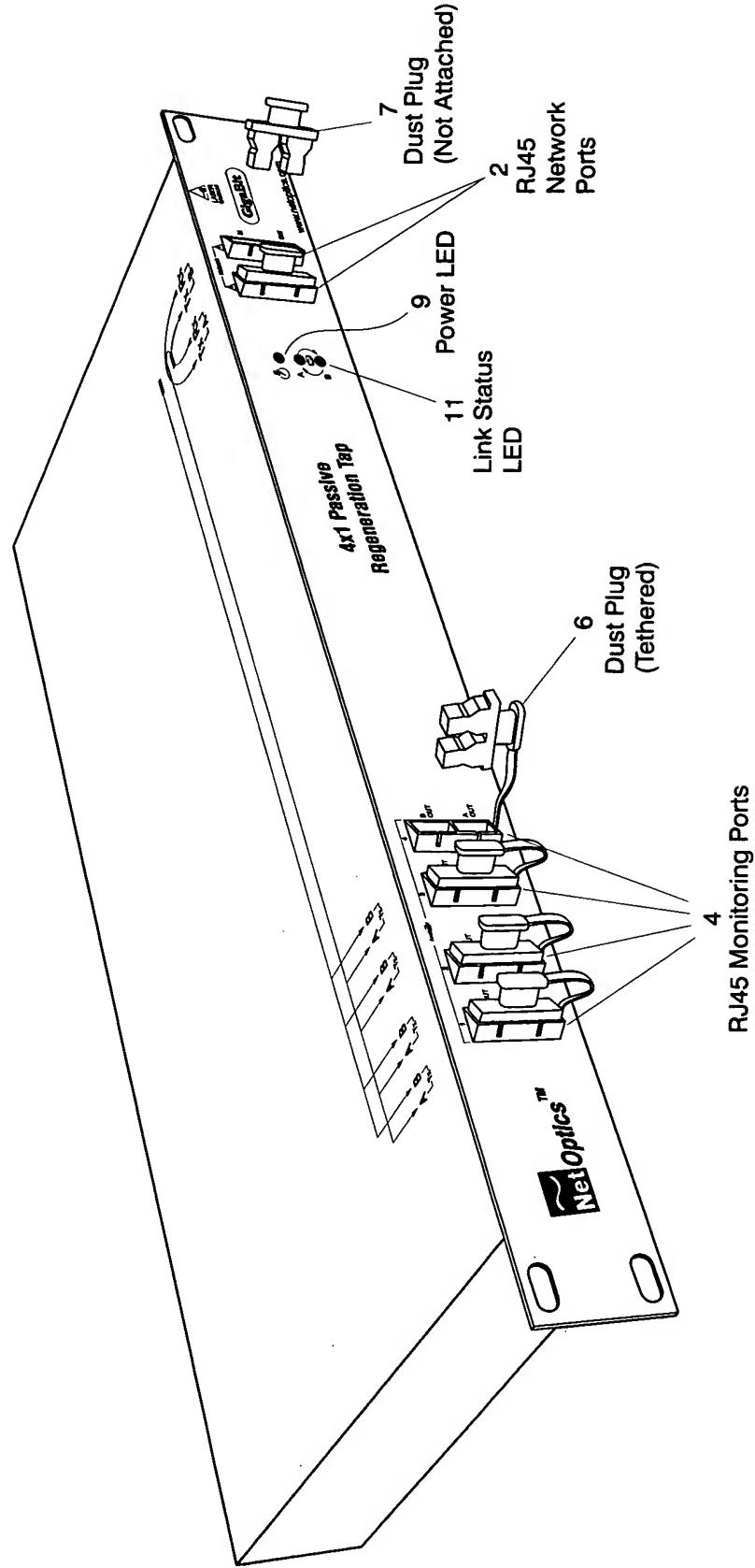


FIG. 1 Exterior front perspective view

# Replacement Sheet—Annotated Sheets

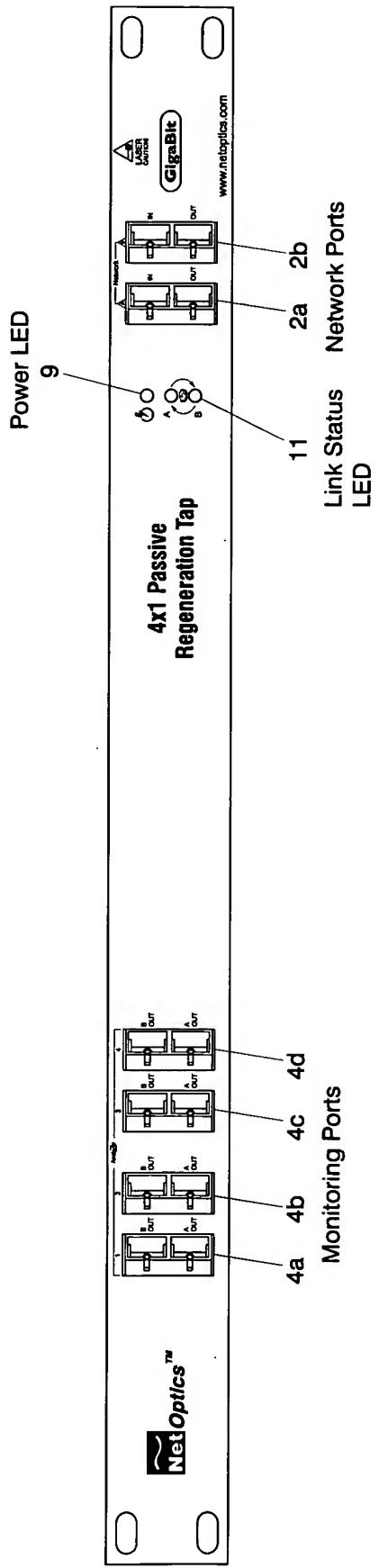


FIG. 2 Exterior front view

## Replacement Sheet—Annotated Sheets

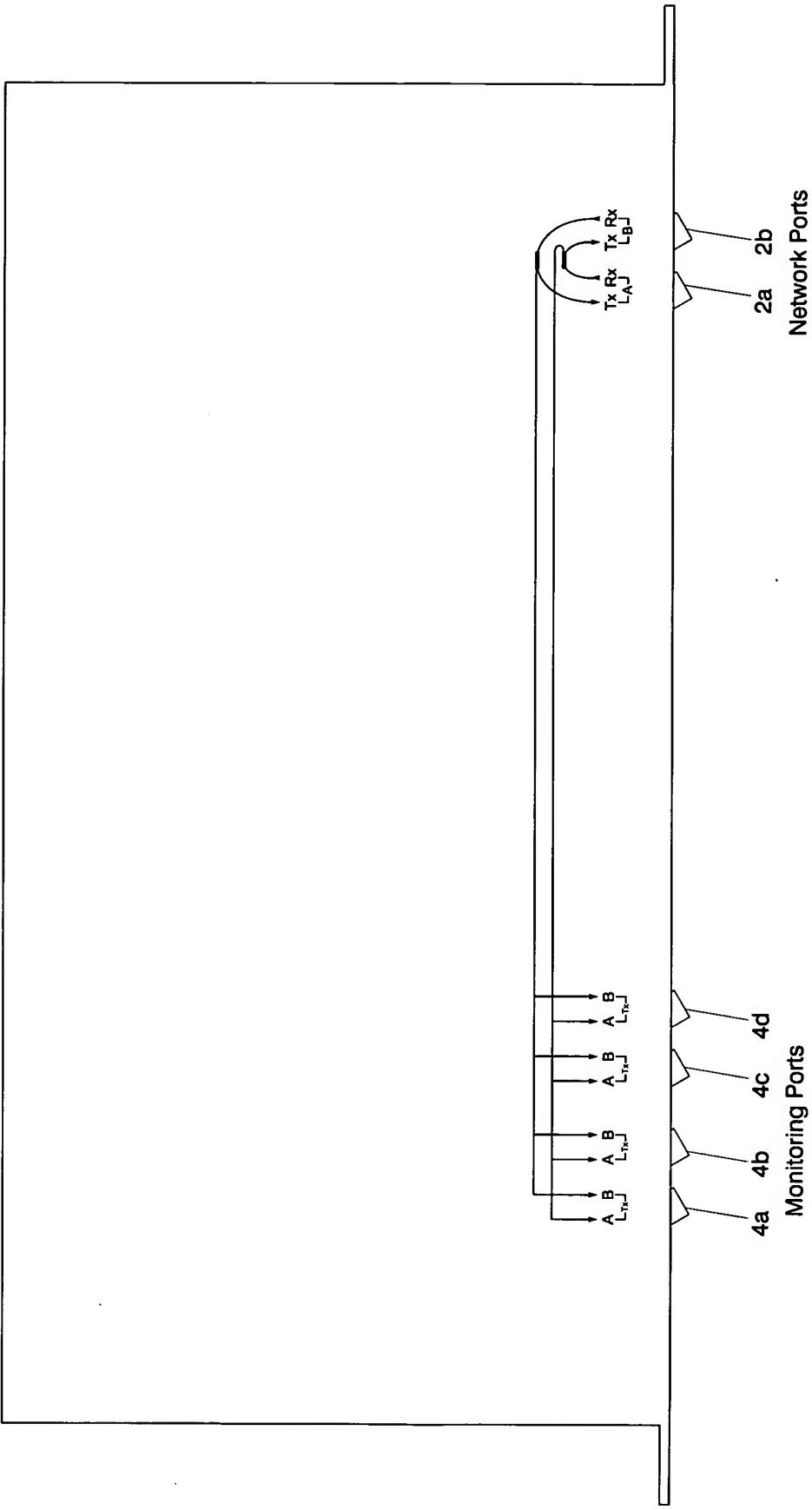


FIG. 3    Exterior top view

## Replacement Sheet—Annotated Sheets

Power Supply Connections  
17  
18

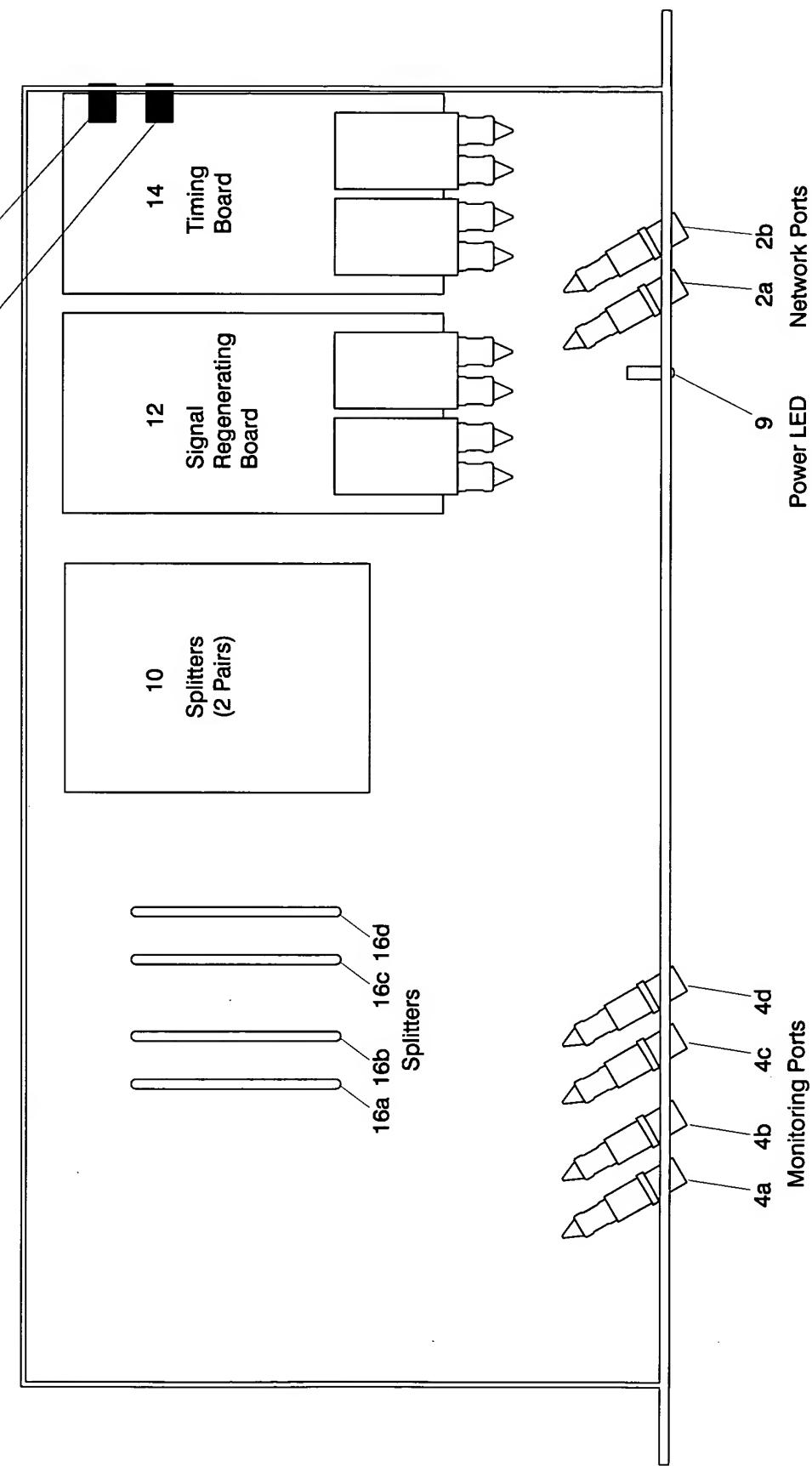
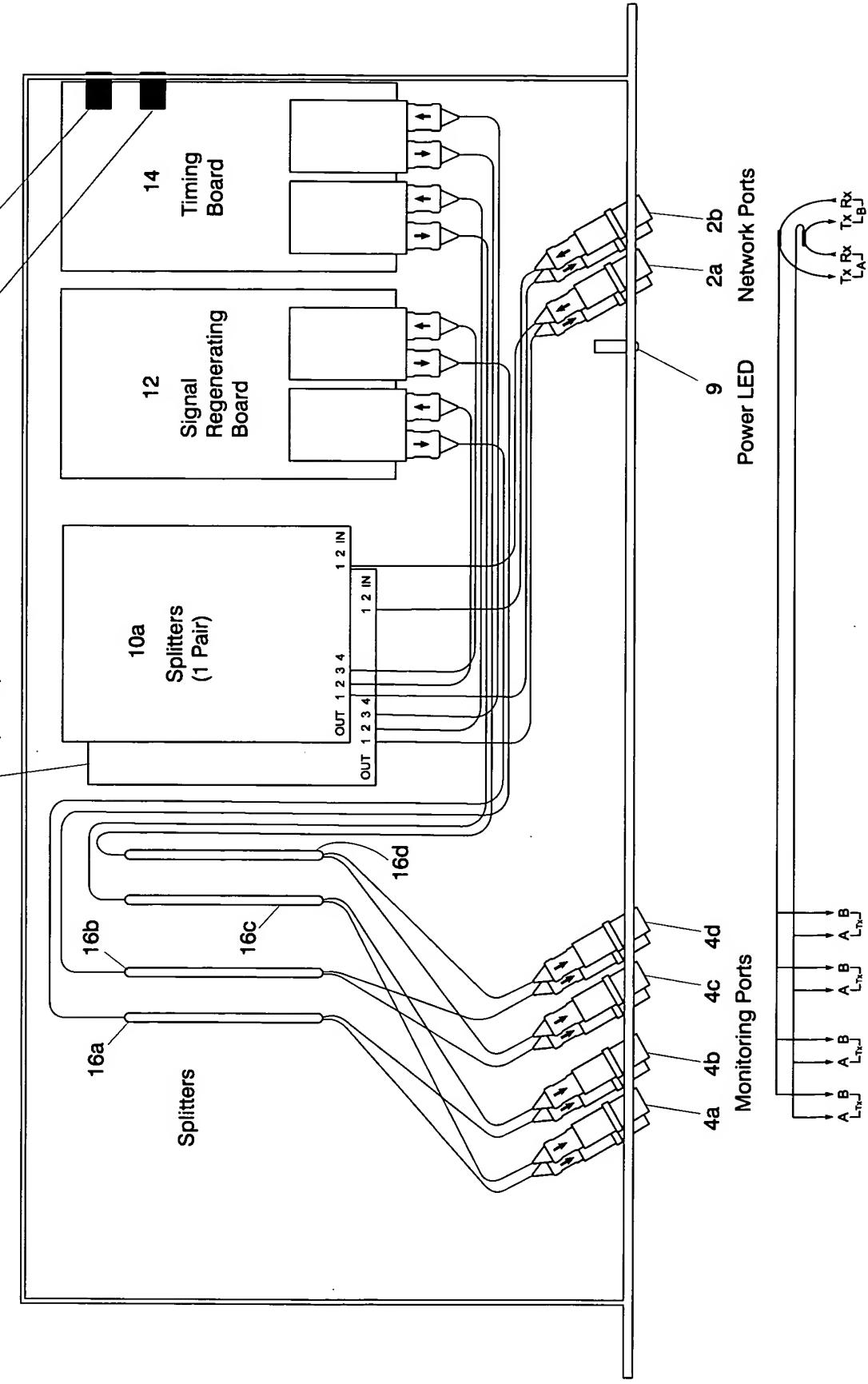


FIG. 4 Interior top view

## Replacement Sheet—Annotated Sheets

Power Supply Connections  
17  
18

10b  
Splitters  
(1 Pair)



**FIG. 5** Interior top view with logical cabling paths and port offsets for clarity

# Replacement Sheet—Annotated Sheets

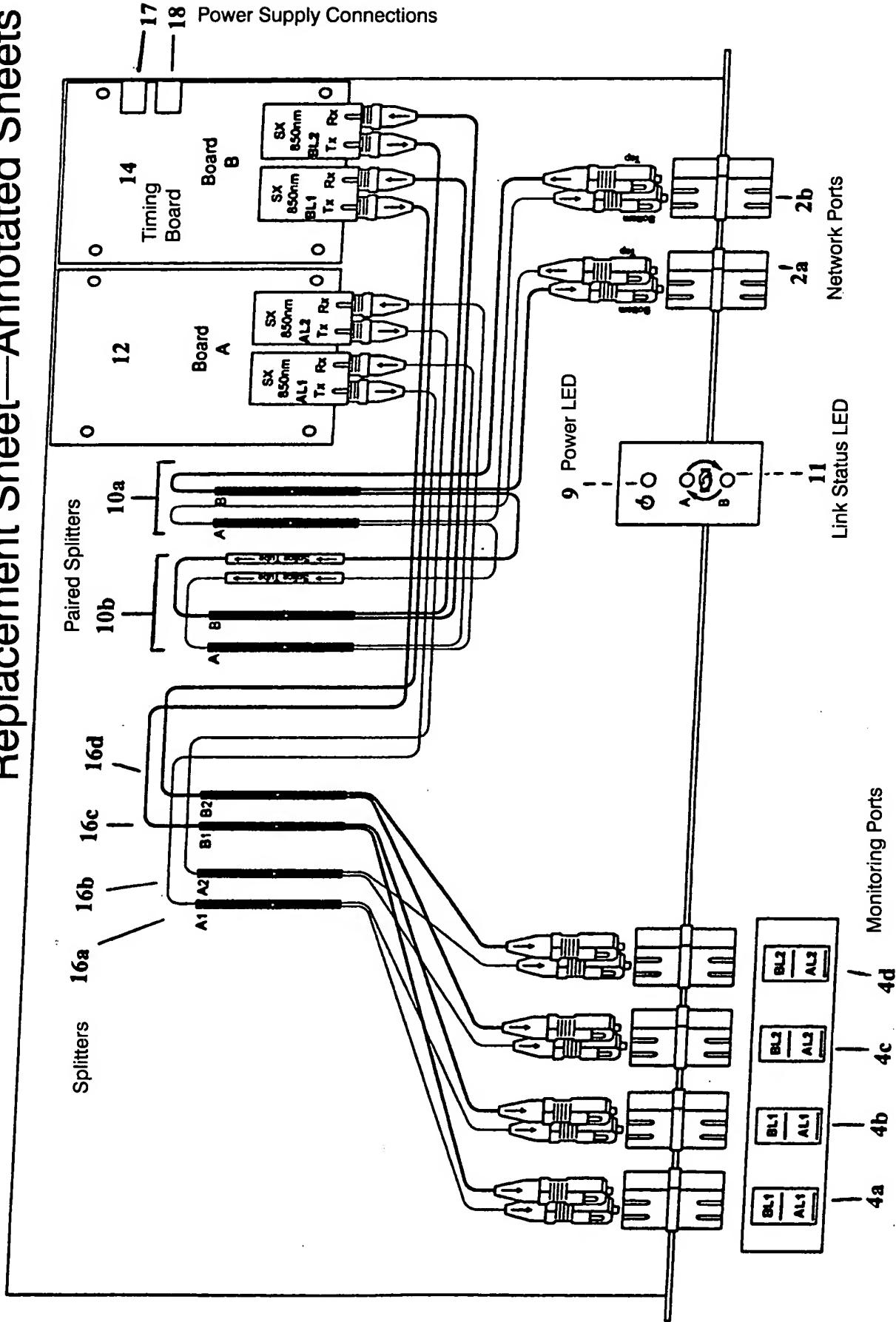
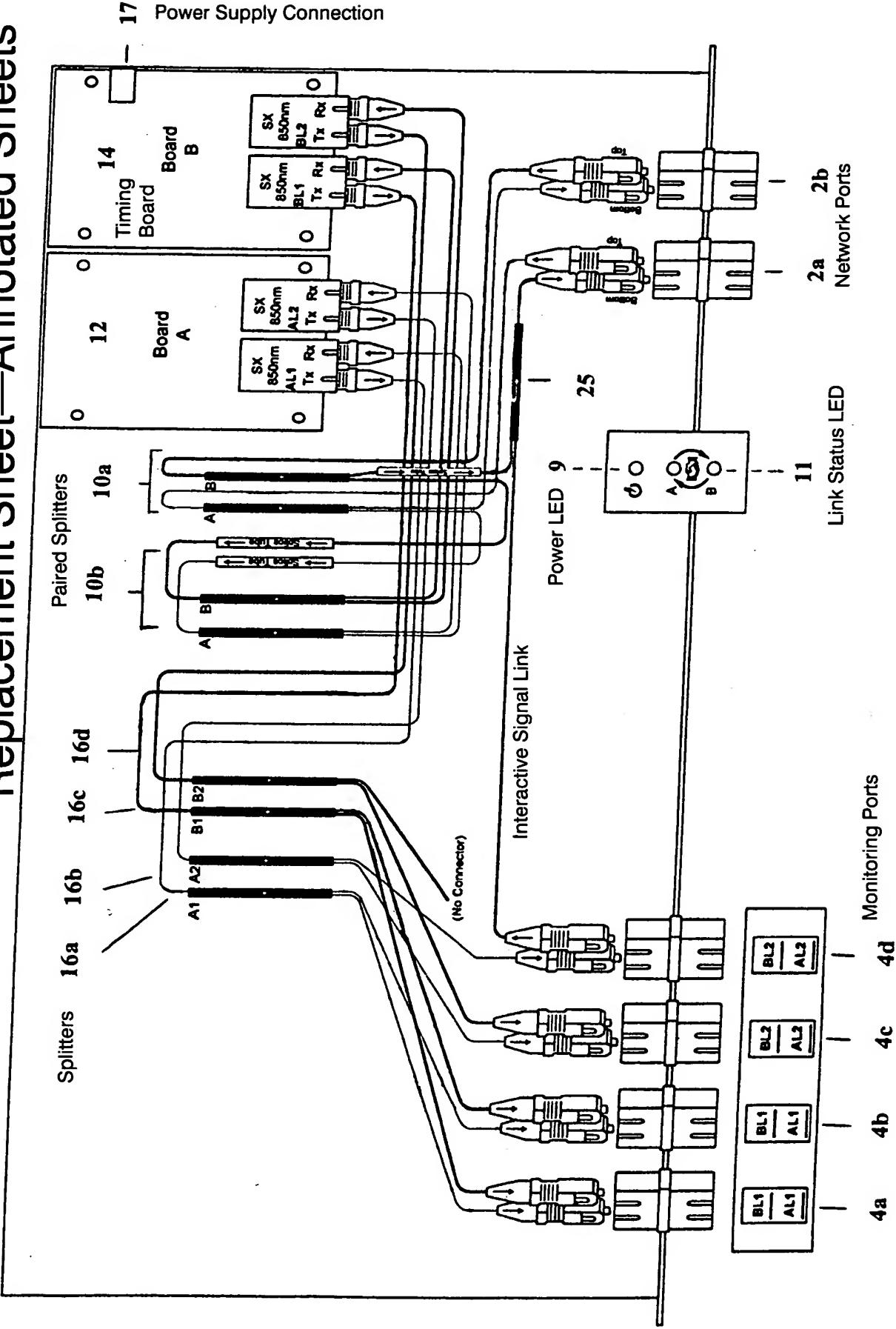


FIG. 6 Interior top view with logical front view

# Replacement Sheet—Annotated Sheets



**FIG. 7** Interior top view with logical front view modified for monitoring input